

CHAPTER 2

Nodal Analysis and Mesh Analysis

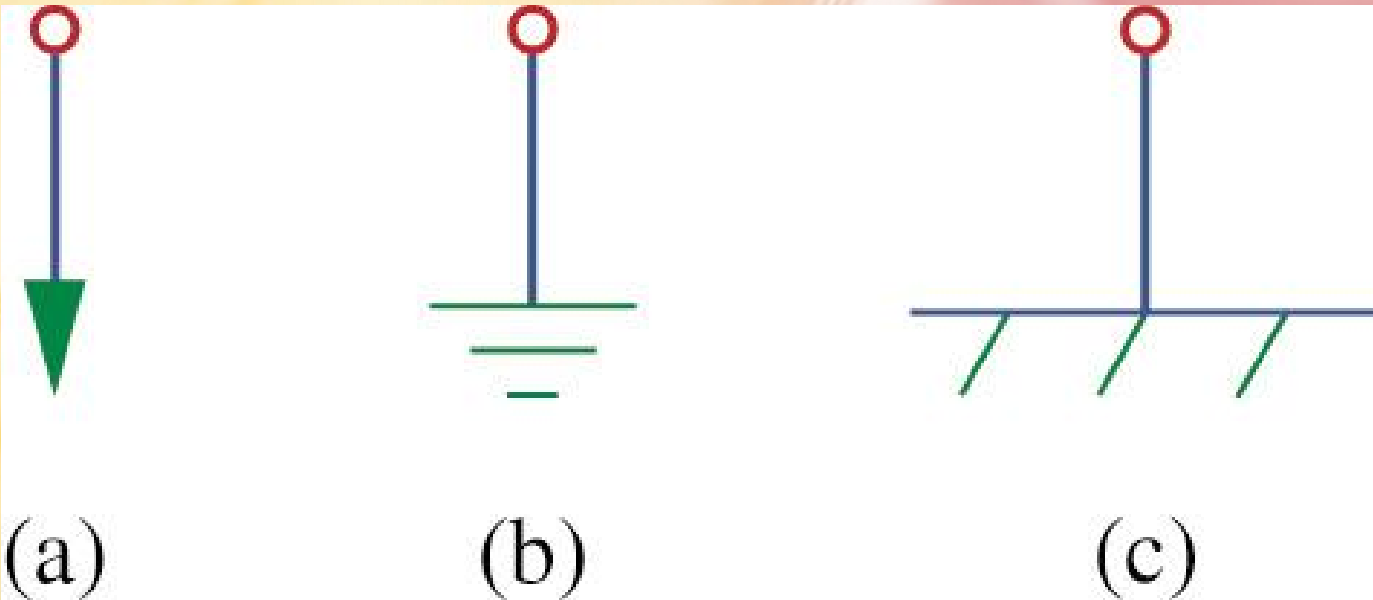
METHODS OF ANALYSIS

- Introduction
- Nodal analysis
- Nodal analysis with voltage source
- Mesh analysis
- Mesh analysis with current source
- Nodal and mesh analyses by inspection
- Nodal versus mesh analysis

STEPS OF NODAL ANALYSIS

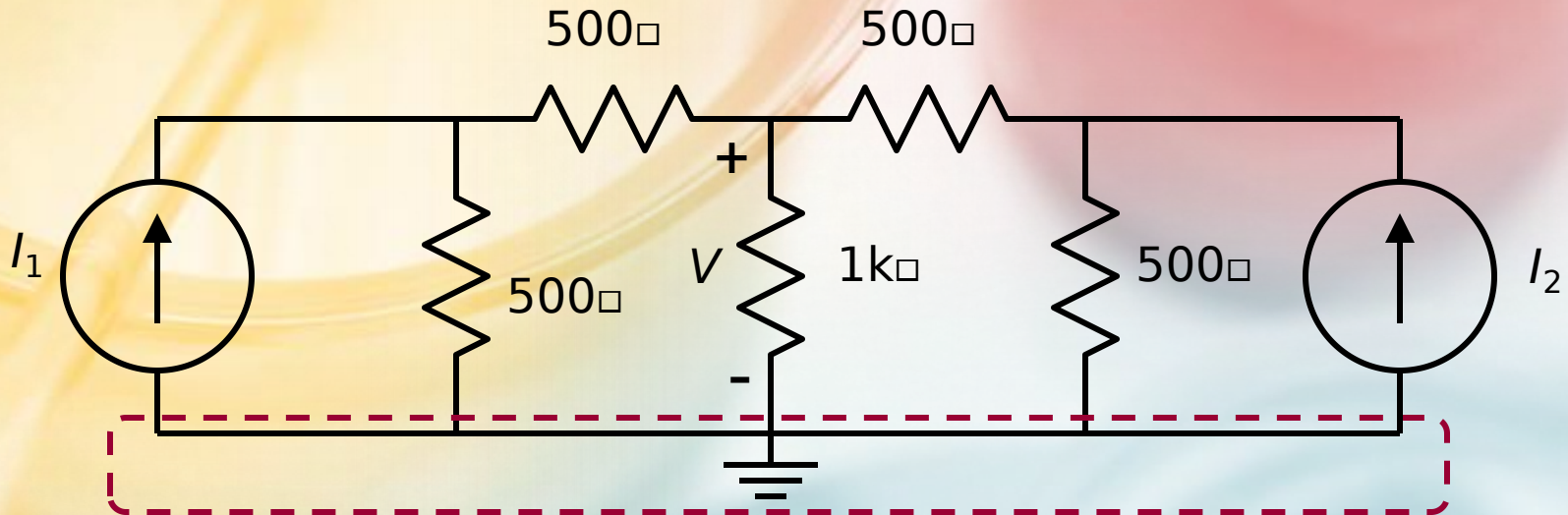
1. Choose a reference (ground) node.
2. Assign node voltages to the other nodes.
3. Apply KCL to each node other than the reference node; express currents in terms of node voltages.
4. Solve the resulting system of linear equations for the nodal voltages.

1. CHOOSE A REFERENCE (GROUND) NODE.



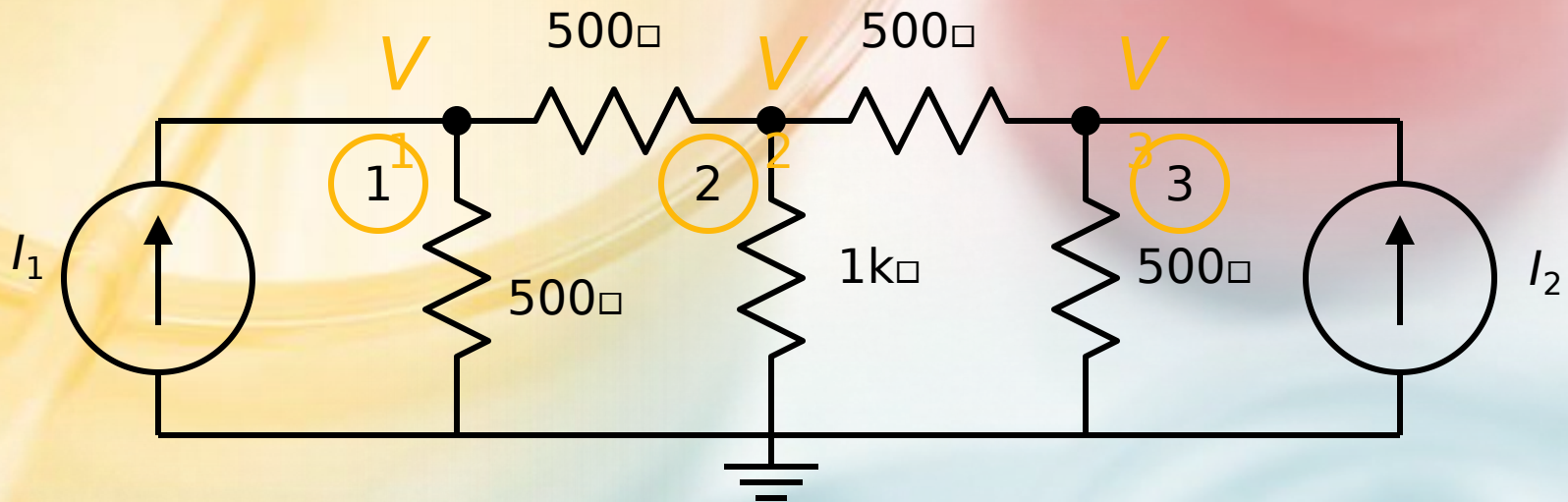
Common symbols for indicating a reference node, (a) common ground, (b) ground, (c) chassis.

1. REFERENCE NODE



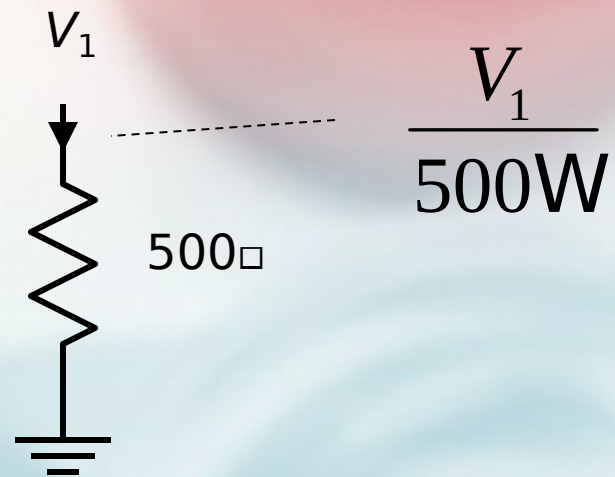
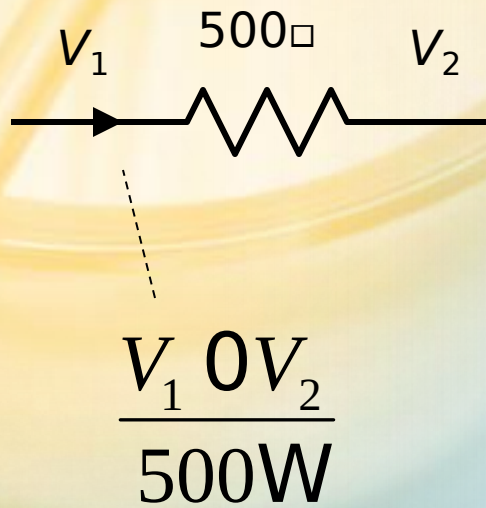
The reference node is called the *ground* node where $V = 0$

2. NODE VOLTAGES

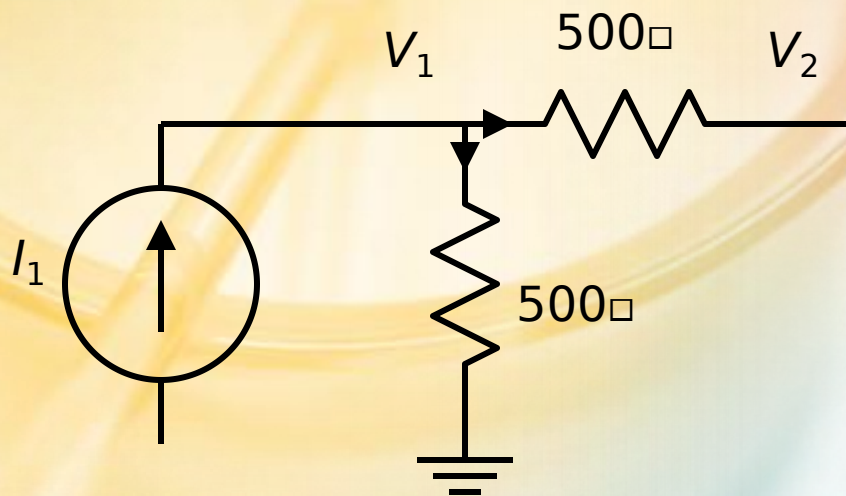


V_1 , V_2 , and V_3 are unknowns for which we solve using KCL

CURRENTS AND NODE VOLTAGES

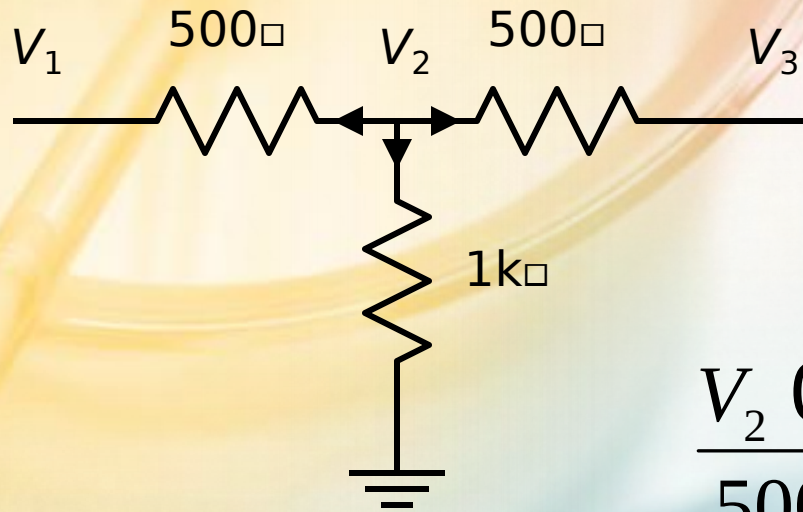


3. KCL AT NODE 1



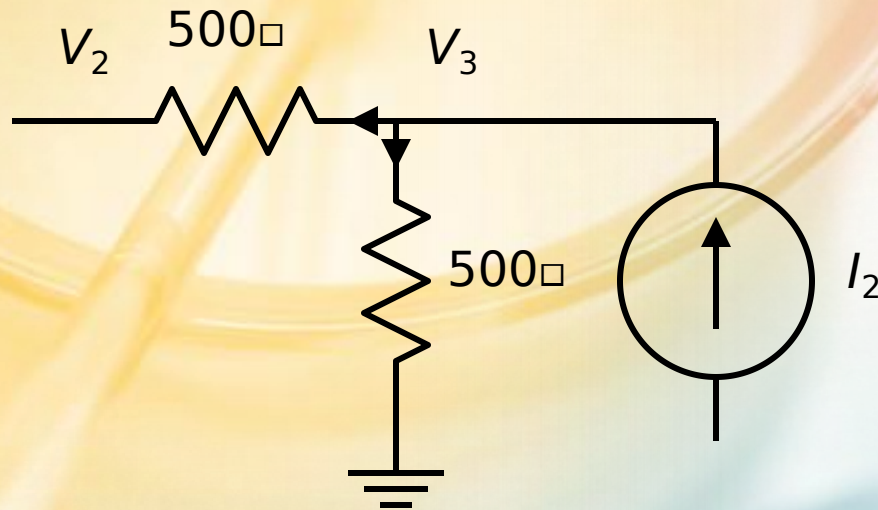
$$I_1 = \frac{V_1}{500\ \Omega} + \frac{V_1 - V_2}{500\ \Omega}$$

3. KCL AT NODE 2



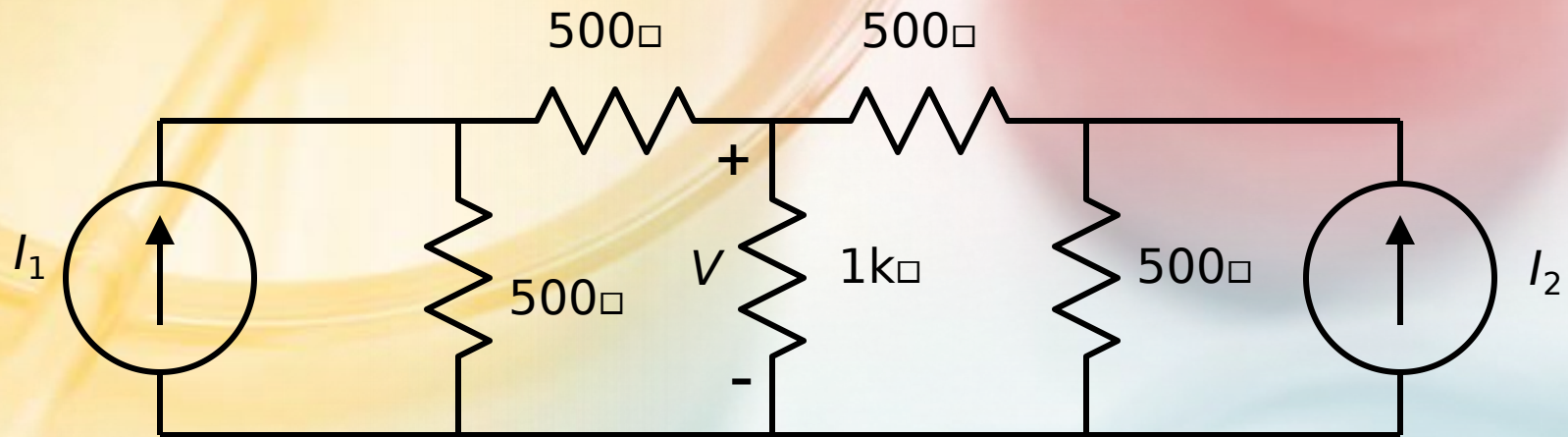
$$\frac{V_2 - 0V_1}{500\Omega} + \frac{V_2}{1k\Omega} + \frac{V_2 - 0V_3}{500\Omega} = 0$$

3. KCL AT NODE 3



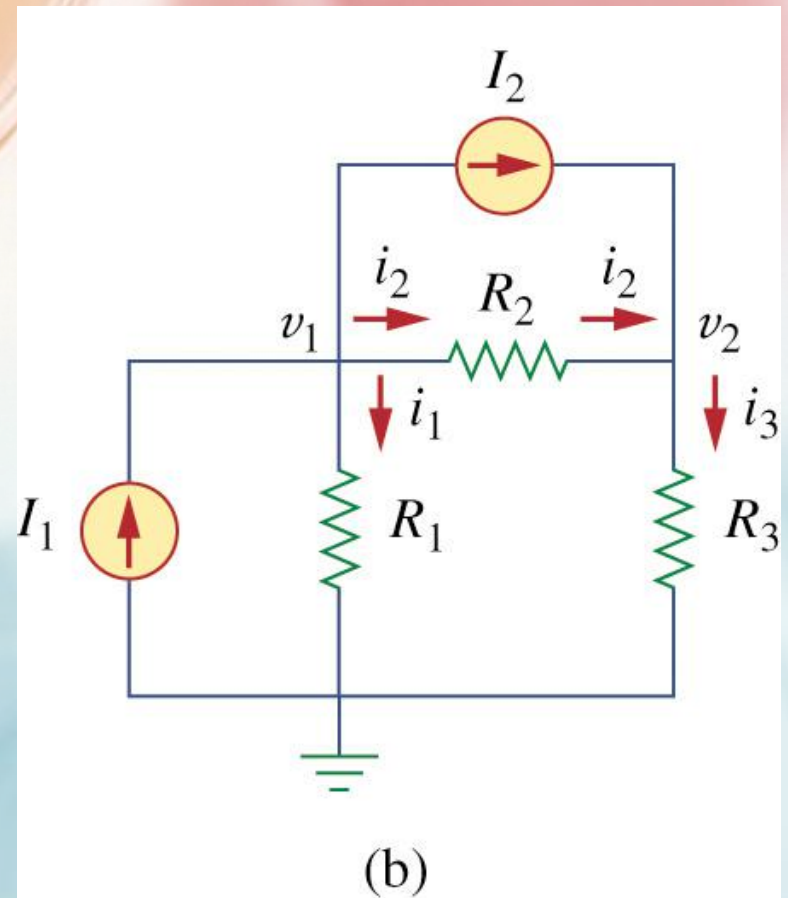
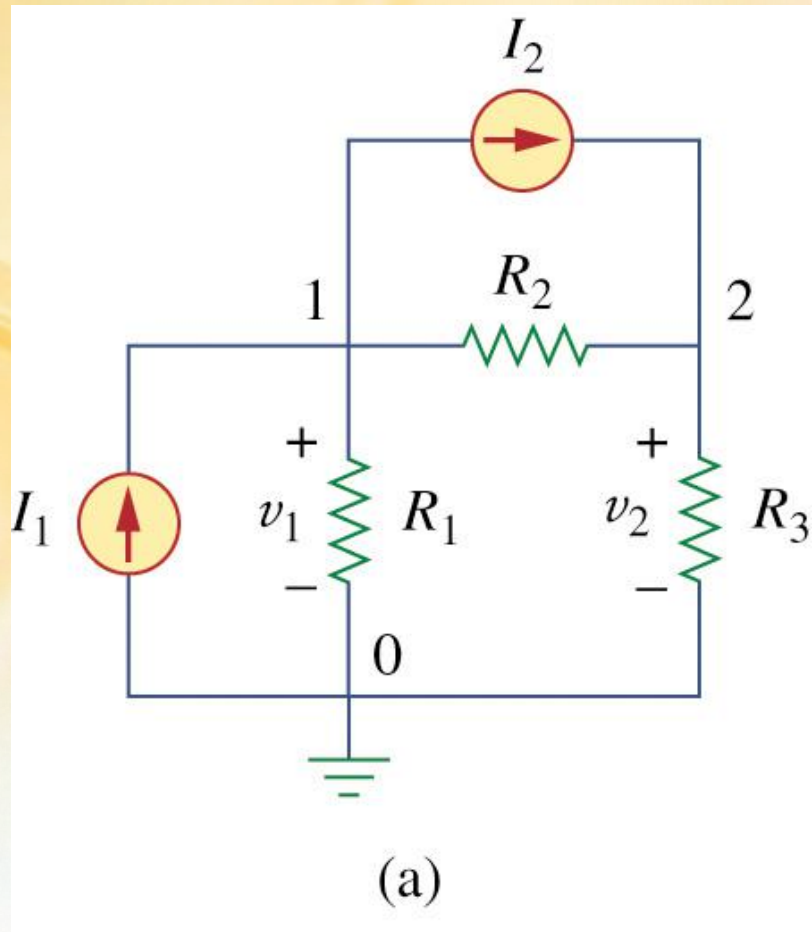
$$\frac{V_3 - 0}{500\Omega} - \frac{V_3}{500\Omega} = 6I_2$$

4. SUMMING CIRCUIT SOLUTION



Solution: $V = 167I_1 + 167I_2$

TYPICAL CIRCUIT FOR NODAL ANALYSIS



NODAL ANALYSIS

$$I_1 \quad \text{6} \quad I_2 \quad \cdot \quad i_1 \quad \cdot \quad i_2$$

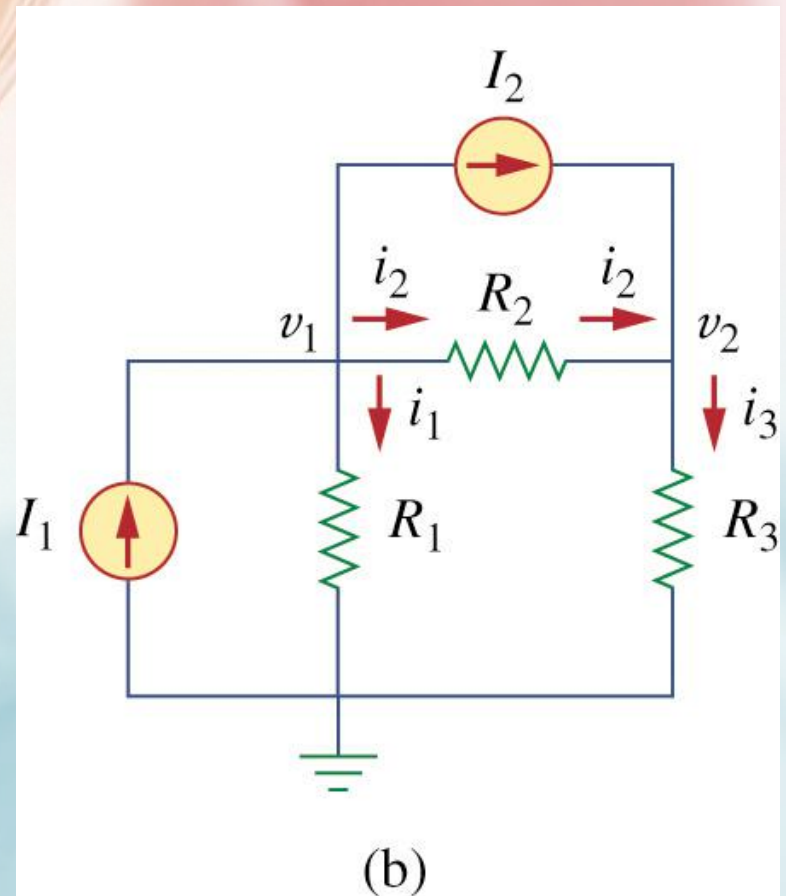
$$I_2 \quad \cdot \quad i_2 \quad \text{6} \quad i_3$$

$$i \quad \text{6} \quad \frac{v_{\text{higher}} \quad 0 \quad v_{\text{lower}}}{R}$$

$$i_1 \quad \text{6} \quad \frac{v_1 \quad 0 \quad 0}{R_1} \quad \text{or} \quad i_1 \quad \text{6} \quad G_1 v_1$$

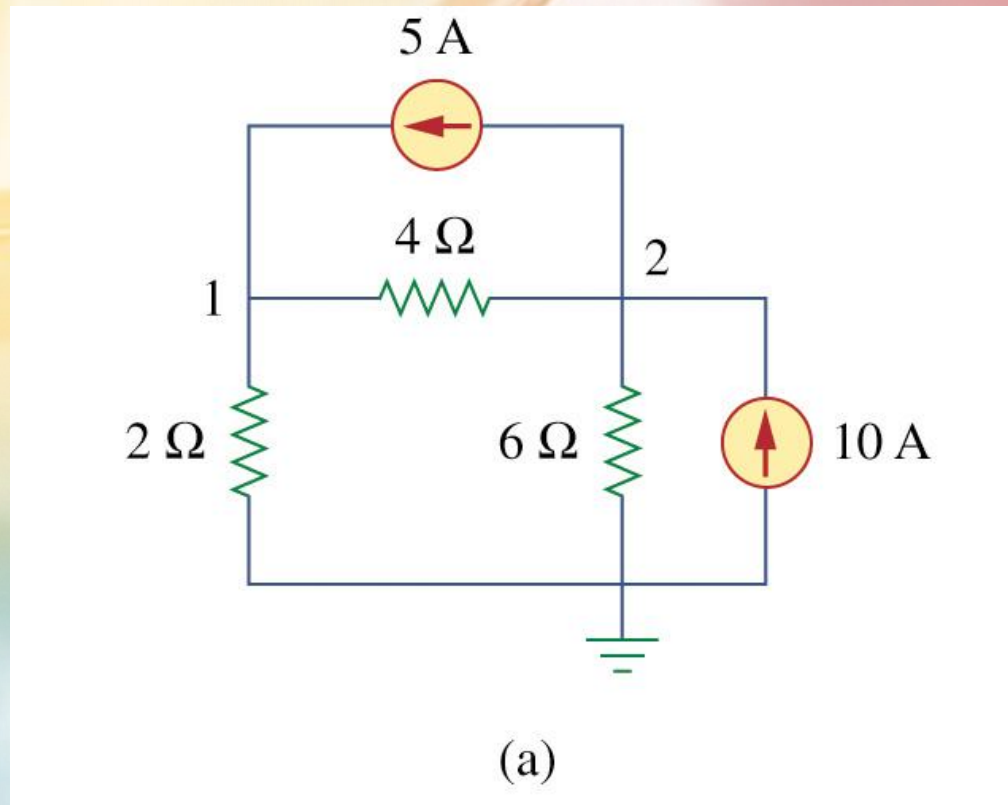
$$i_2 \quad \text{6} \quad \frac{v_1 \quad 0 \quad v_2}{R_2} \quad \text{or} \quad i_2 \quad \text{6} \quad G_2 (v_1 \quad 0 \quad v_2)$$

$$i_3 \quad \text{6} \quad \frac{v_2 \quad 0 \quad 0}{R_3} \quad \text{or} \quad i_3 \quad \text{6} \quad G_3 v_2$$



EXAMPLE

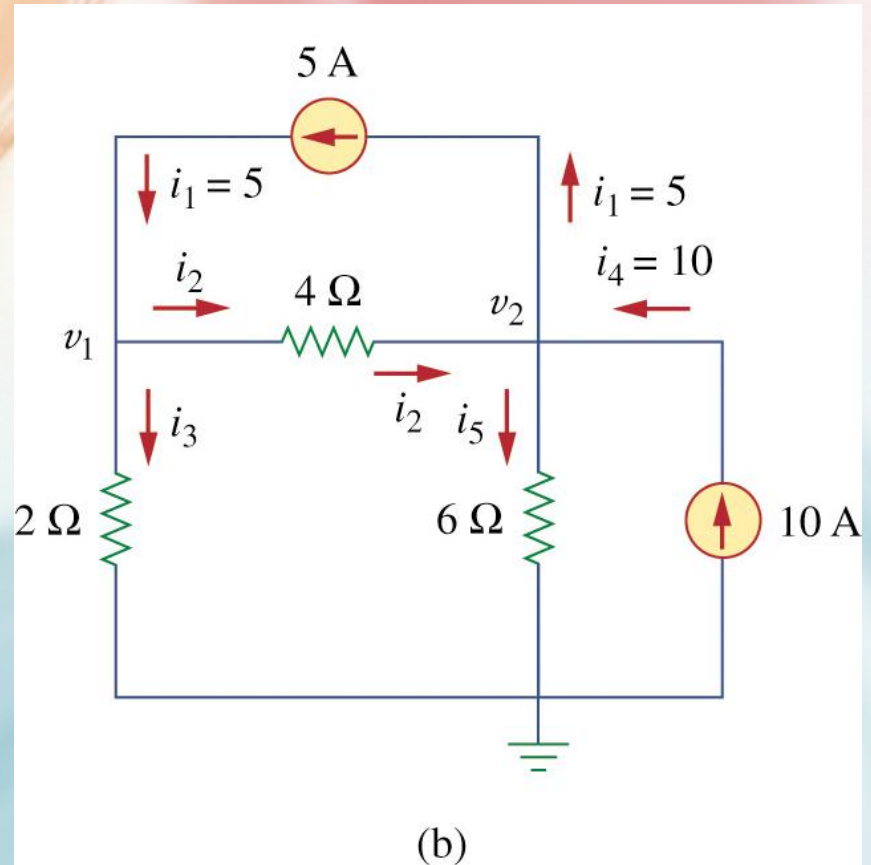
- Calculate the node voltage in the circuit shown in Fig.



SOLUTION

- At node 1

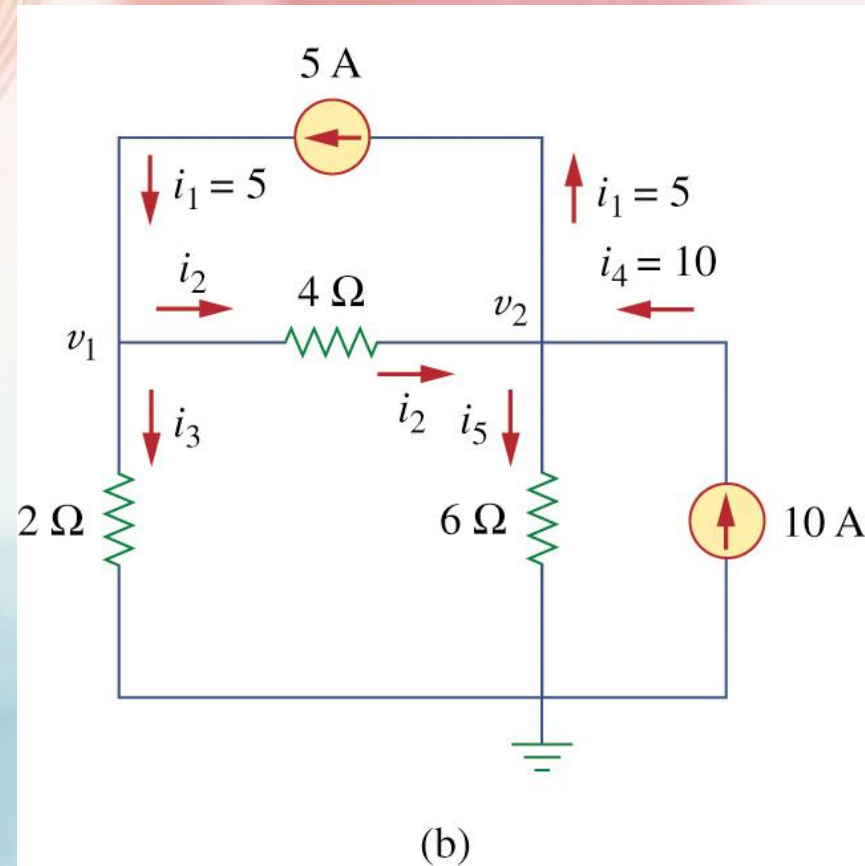
$$\mathbf{p} \begin{matrix} & i_1 & 6i_2 & \cdot & i_3 \\ 5 & 6 & \frac{v_1}{4} & 0 & \frac{v_1}{2} \\ 0 & 0 & v_2 & 0 & 0 \end{matrix}$$



SOLUTION

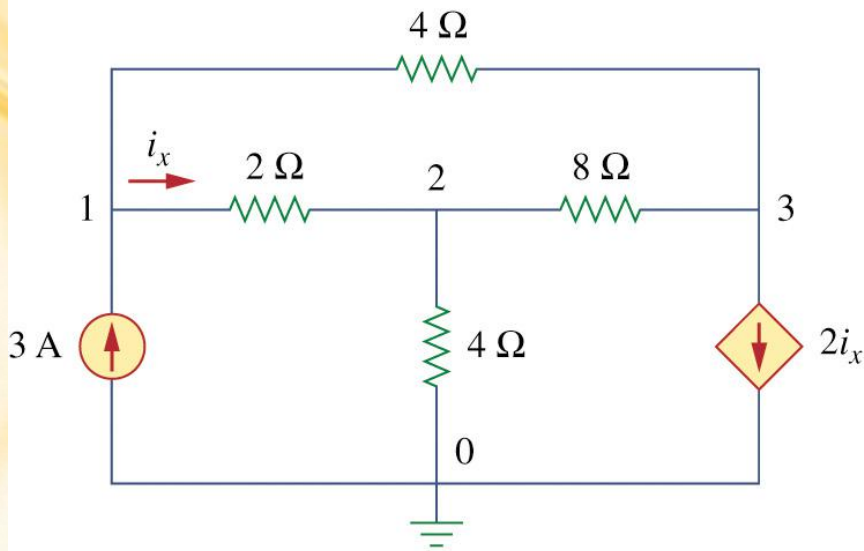
- At node 2

$$\mathbf{p} \begin{bmatrix} i_2 & i_4 & 6i_1 & i_5 \\ 5 & 6 & 0 & 0 \\ v_2 & v_1 & v_2 & 0 \\ 4 & 6 & 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} v_2 \\ v_1 \\ v_2 \\ 0 \end{bmatrix}$$

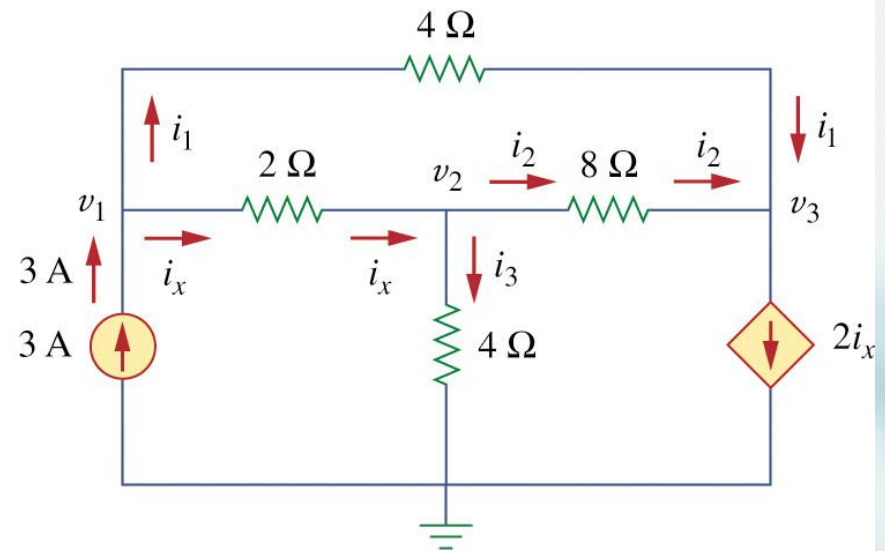


EXAMPLE

- Determine the voltage at the nodes in Fig. below



(a)

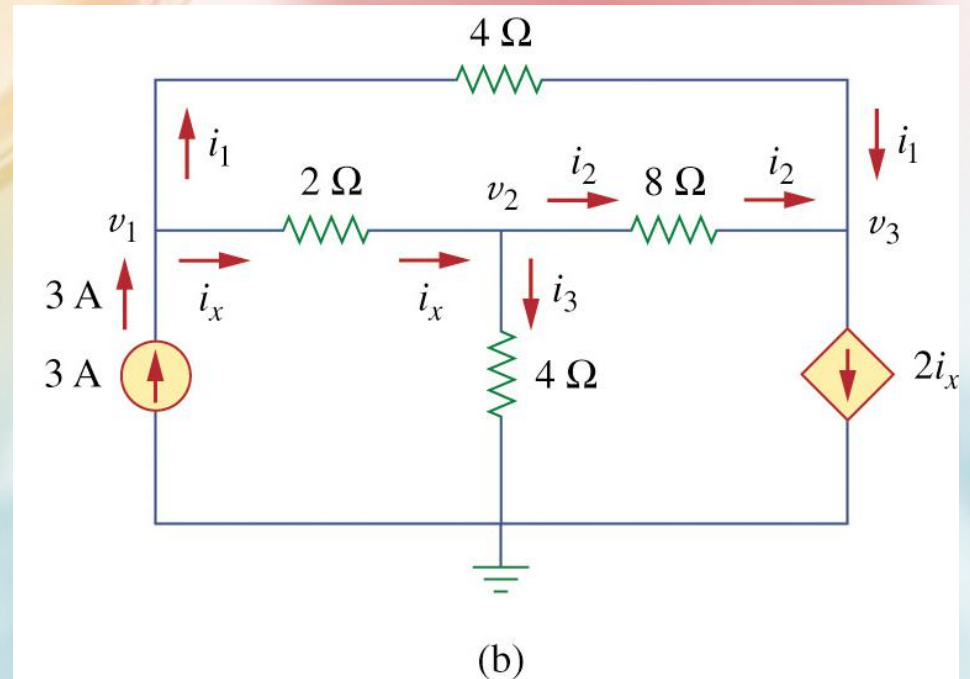


(b)

SOLUTION

- At node 1,

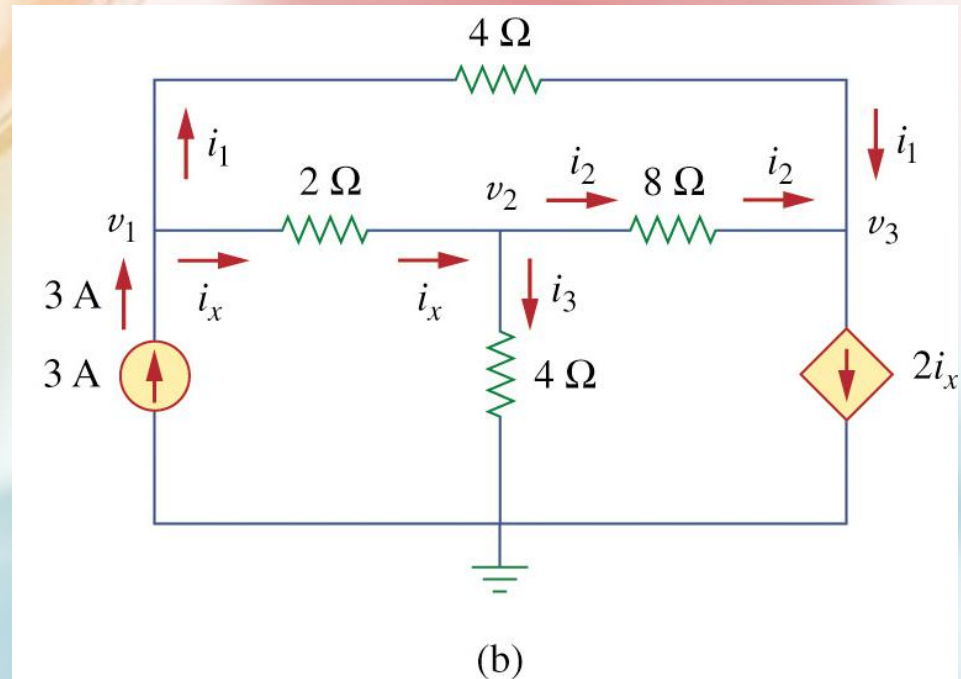
$$36 \frac{v_1}{4} + 36 \frac{v_3}{4} = 36 \frac{v_1}{2} + 36 \frac{v_2}{2}$$



SOLUTION

- At node 2

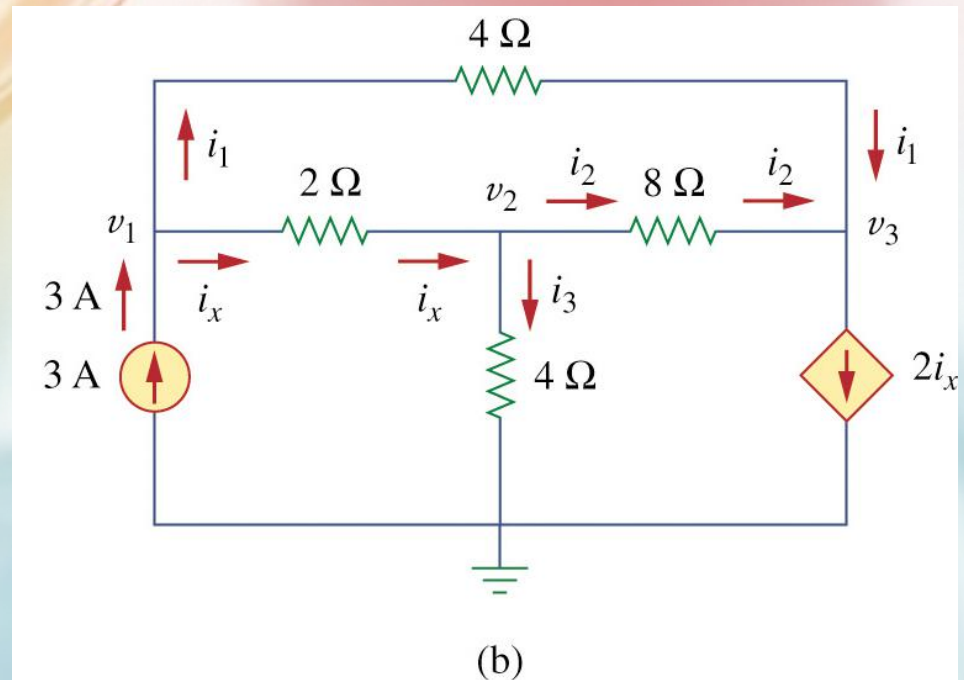
$$\mathbf{p} \begin{matrix} & & i_x & 6i_2 & \cdot & i_3 \\ & v_1 & 0 & v_2 & 6 & v_2 & 0 & v_3 \\ & 2 & & 8 & & 4 & & \end{matrix} \cdot \begin{matrix} v_2 & 0 & 0 \\ v_2 & 0 & 0 \\ v_2 & 0 & 0 \end{matrix}$$



SOLUTION

- At node 3

$$p \frac{v_1 \ 0 \ v_3}{4} \cdot \frac{v_2 \ 0 \ v_3}{8} \cdot 6 \frac{2(v_1 \ 0 \ v_2)}{2}$$

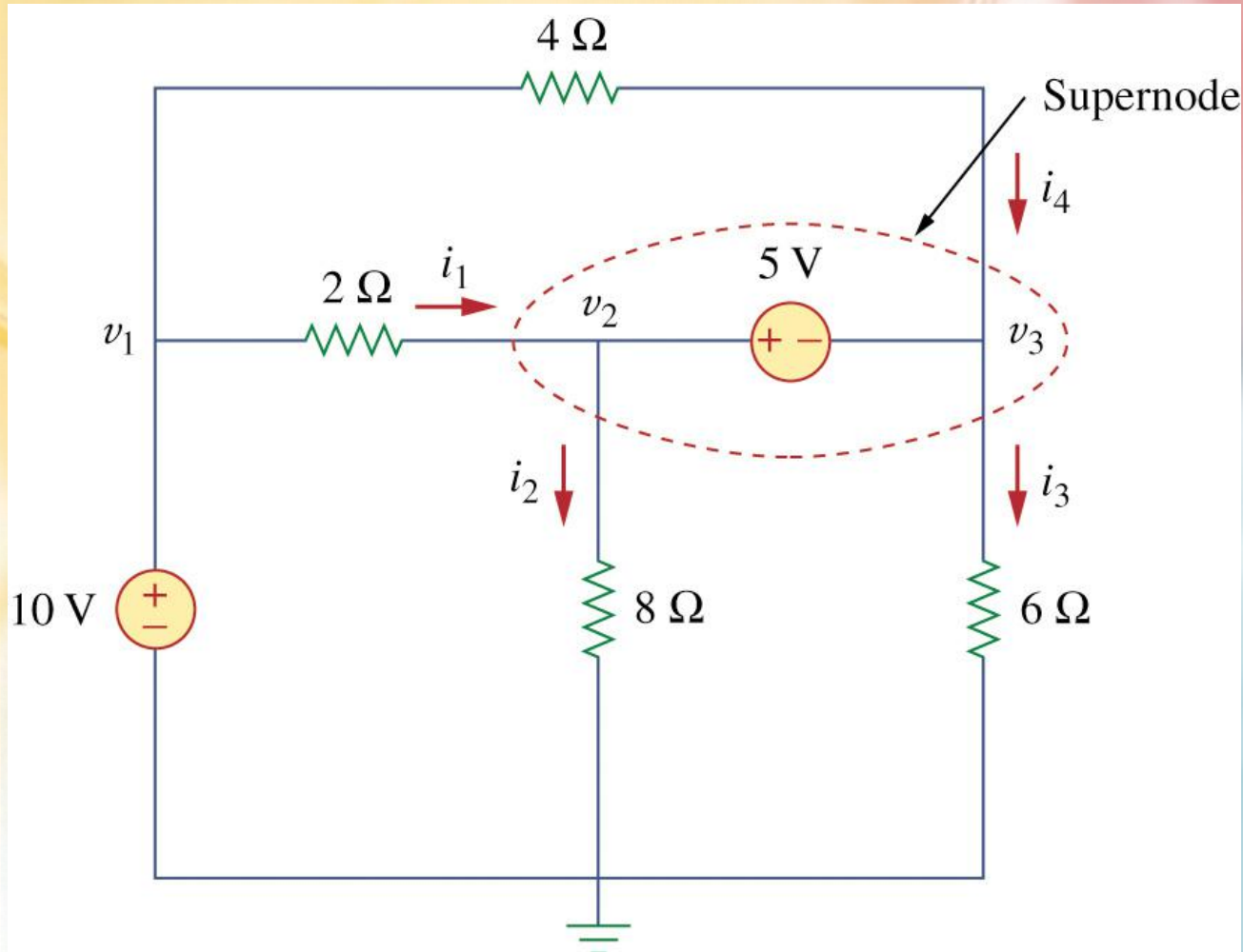


NODAL ANALYSIS WITH VOLTAGE SOURCES

- Case 1: The voltage source is connected **between a nonreference node and the reference node**: The nonreference node voltage is equal to the magnitude of voltage source and the number of unknown nonreference nodes is reduced by one.
- Case 2: The voltage source is connected **between two nonreferenced nodes**: a generalized node (**supernode**) is formed.

VOLTAGE SOURCES

A circuit with a supernode.

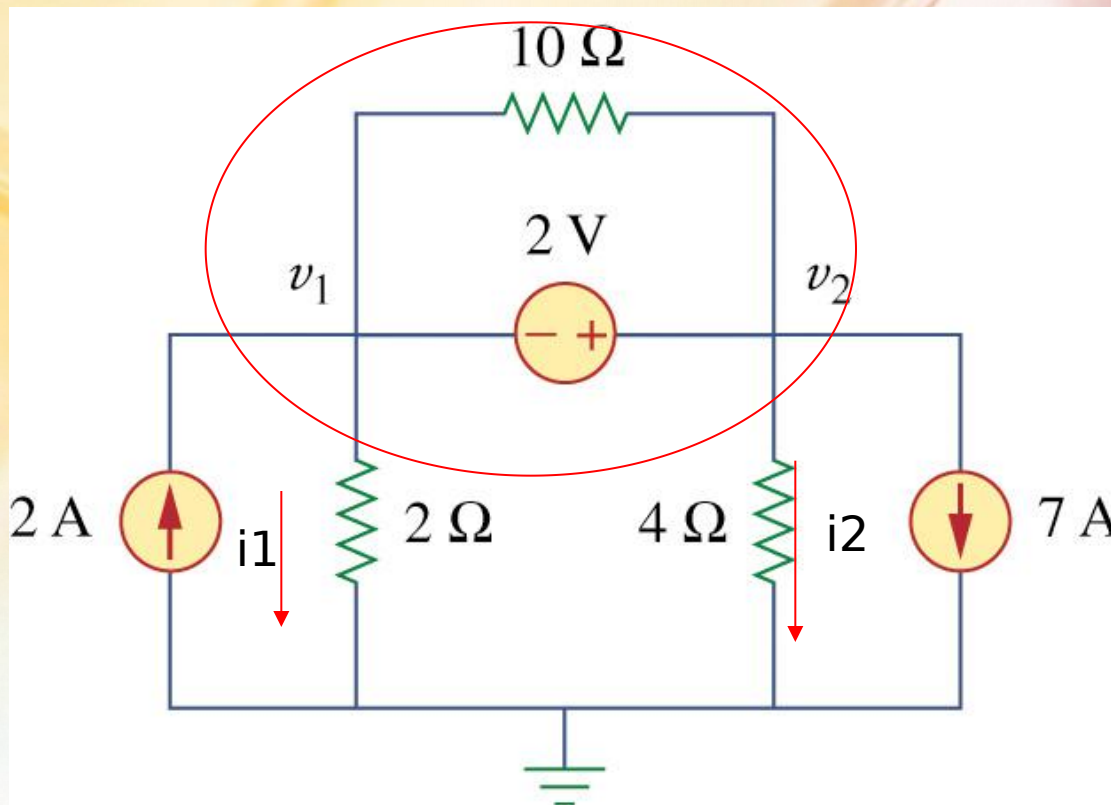


SUPERMODE

- A **supernode** is formed by **enclosing** a (dependent or independent) voltage source connected between two nonreference nodes and **any elements connected in parallel with it**.
- The required **two equations** for regulating the two nonreference node voltages are obtained by the KCL of the supernode and the **relationship of node voltages due to the voltage source**.

EXAMPLE

- For the circuit shown in Fig., find the node voltages.



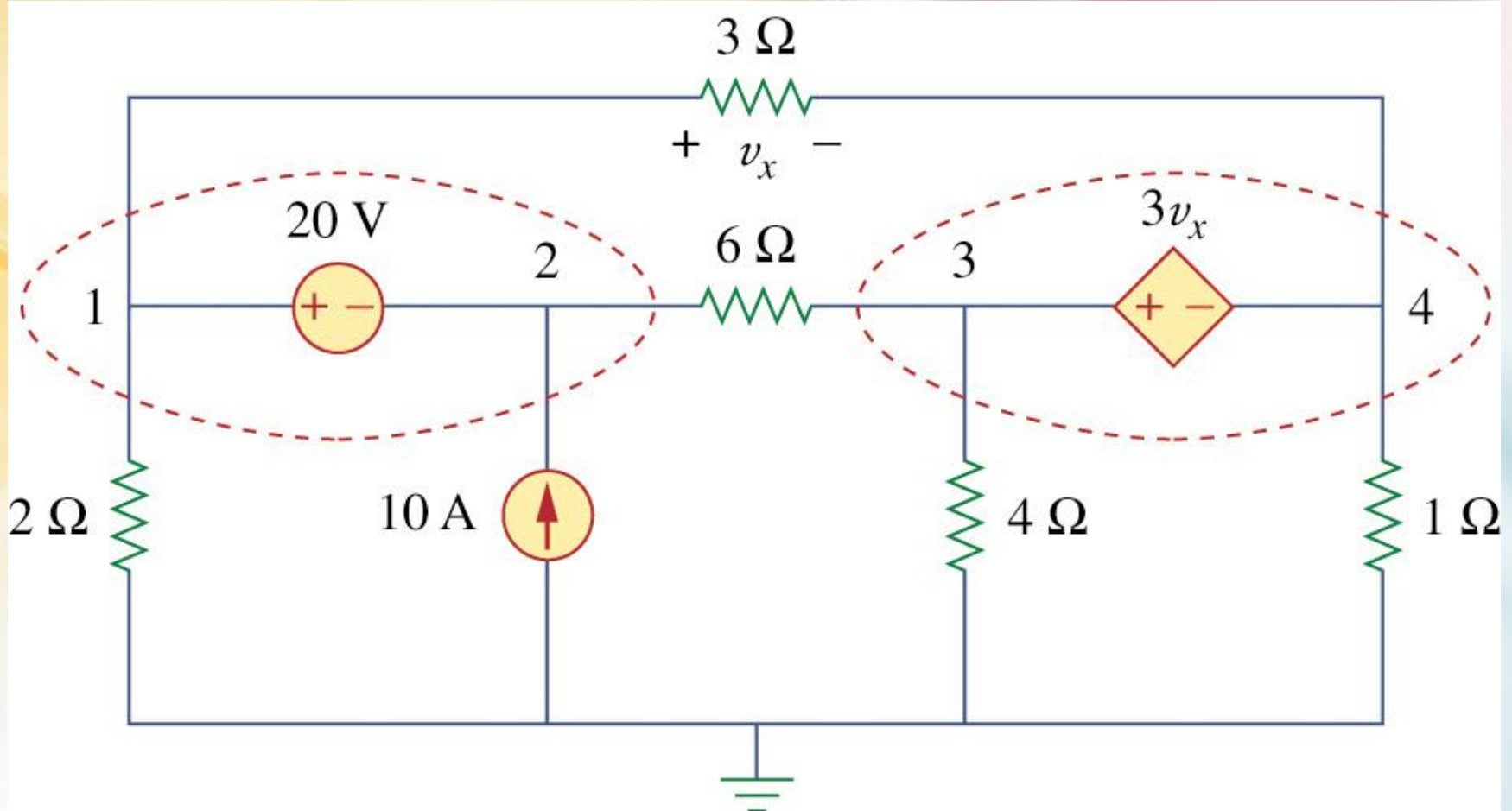
$$2 \quad 0 \quad 7 \quad 0 \quad i_1 \quad 0 \quad i_2 \quad 6 \quad 0$$

$$2 \quad 0 \quad 7 \quad 0 \quad \frac{v_1}{2} \quad 0 \quad \frac{v_2}{4} \quad 6 \quad 0$$

$$v_1 \quad 0 \quad v_2 \quad 6 \quad 0 \quad 2$$

EXAMPLE

Find the node voltages in the circuit below.

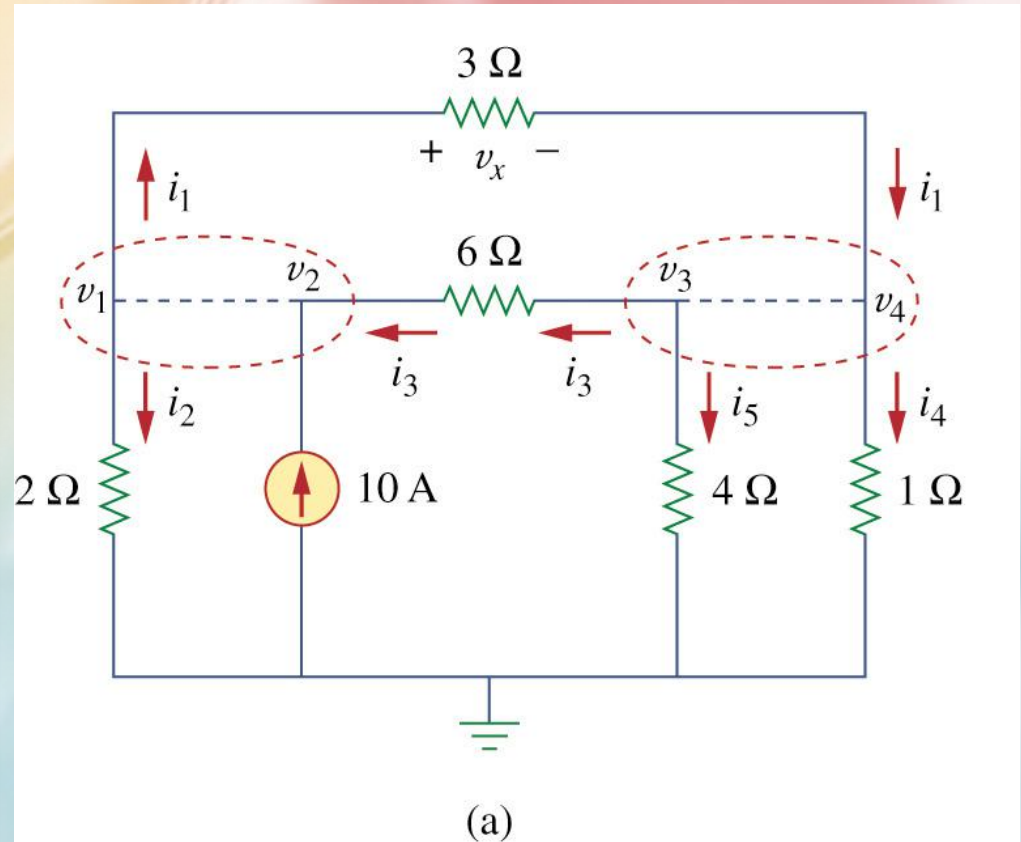


SOLUTION

- At supernode 1-2,

$$\frac{v_3}{6} + \frac{0}{3} + \frac{v_2}{6} + \frac{10}{6} + \frac{v_1}{3} + \frac{0}{2} + \frac{v_4}{2} = \frac{v_1}{2}$$

$$v_1 \quad 0 \quad v_2 \quad 6 \quad 20$$

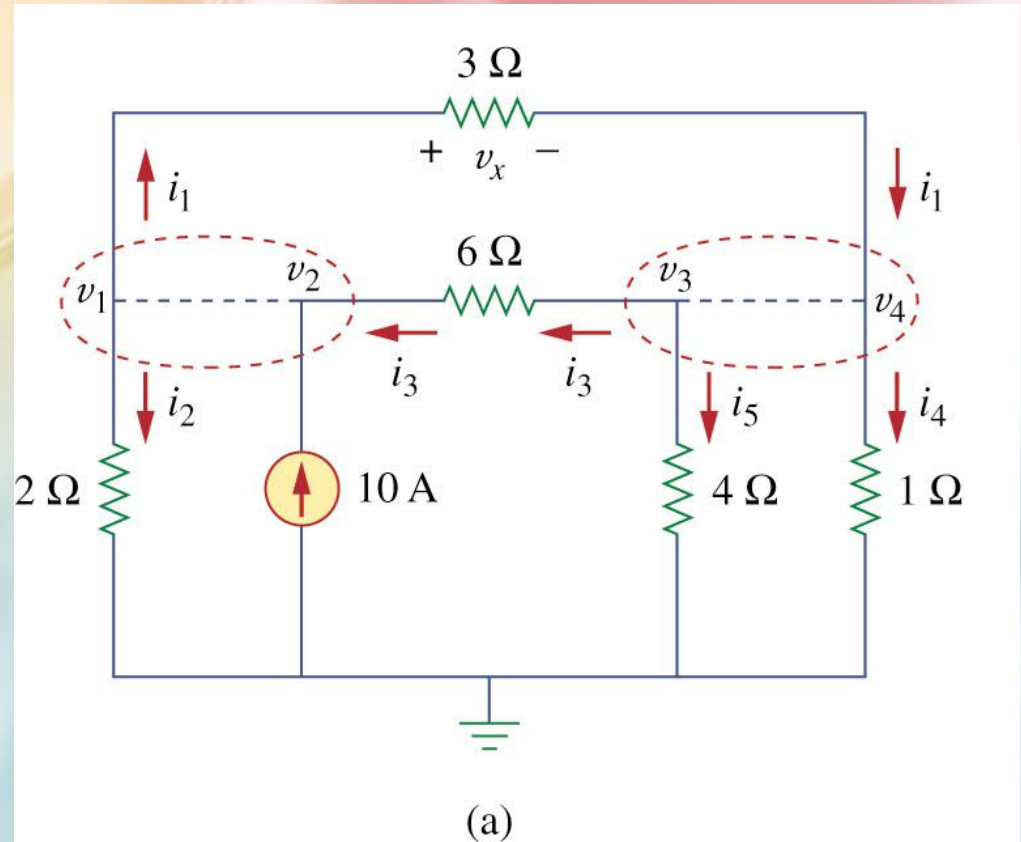


SOLUTION

- At supernode 3-4,

$$\frac{v_1}{3} + \frac{0v_4}{6} + \frac{v_3}{6} + \frac{0v_2}{1} + \frac{v_4}{4} = \frac{v_3}{4}$$

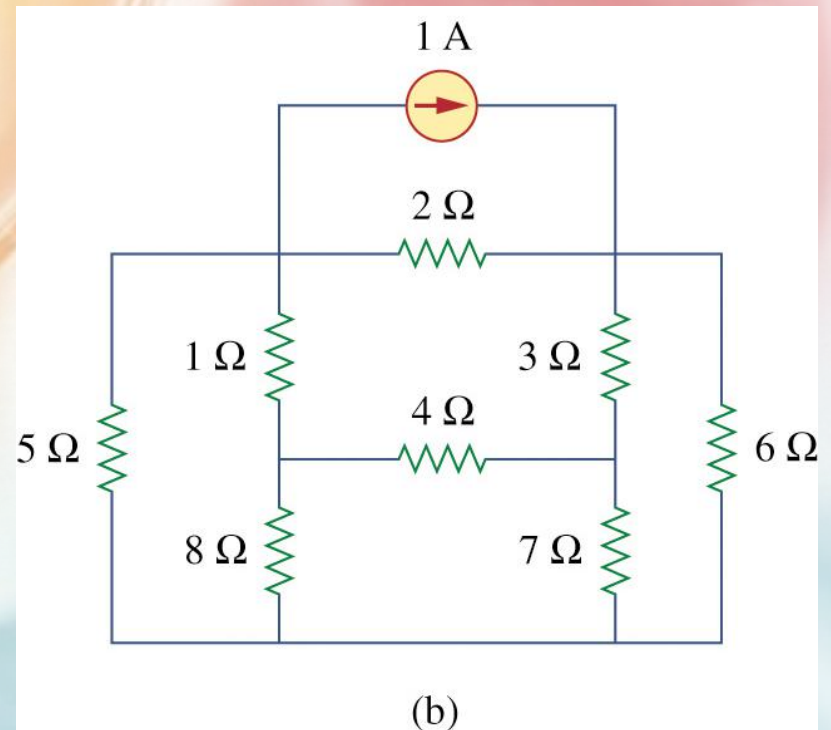
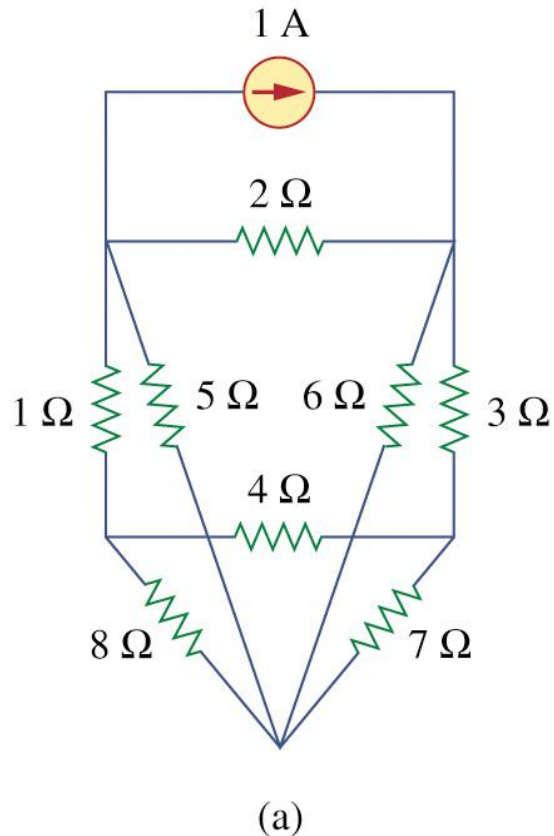
$$v_3 + 0v_4 + 6 + 3(v_1 + 0v_4)$$



MESH ANALYSIS

- Mesh analysis: another procedure for analyzing circuits, applicable to **planar** circuit.
- A Mesh is a loop which does not contain any other loops within it

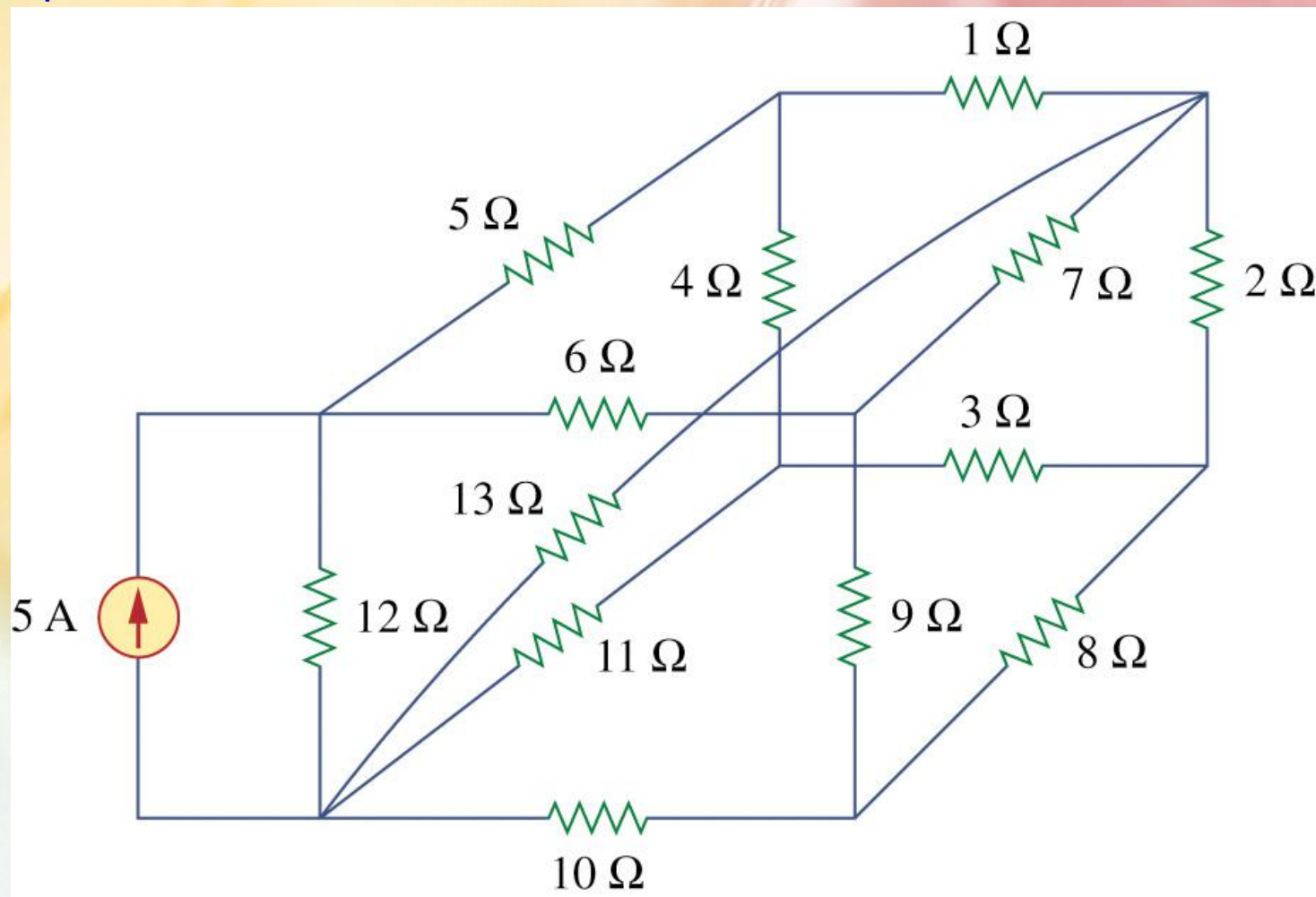
MESH ANALYSIS



- (a) A Planar circuit with crossing branches,
- (b) The same circuit redrawn with no crossing branches.

MESH ANALYSIS

A **nonplanar** circuit.

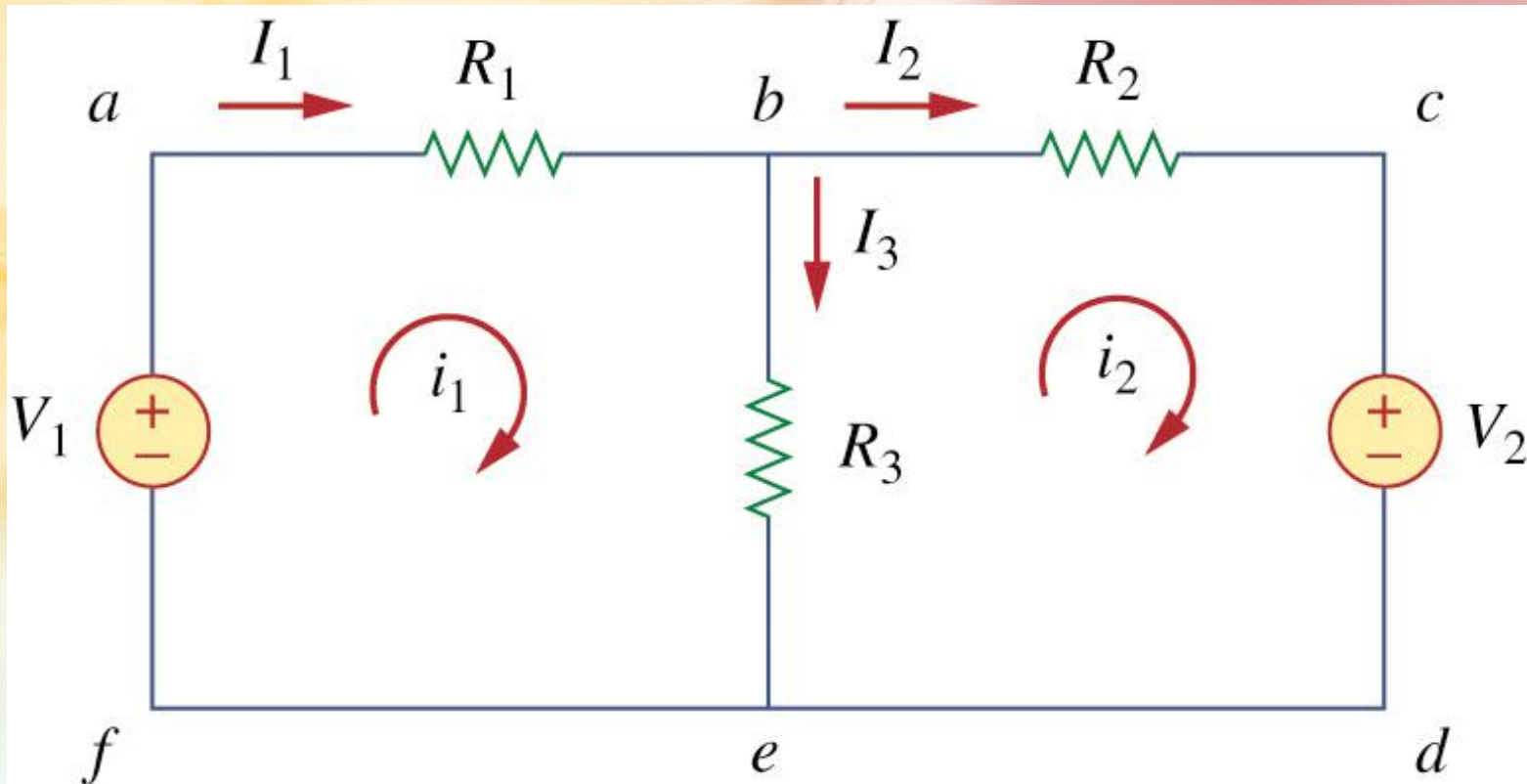


MESH ANALYSIS

- Steps to Determine Mesh Currents:
 1. Assign **mesh currents** i_1, i_2, \dots, i_n to the n meshes.
 2. Apply **KVL** to each of the n meshes. Use **Ohm's law** to express the voltages in terms of the mesh currents.
 3. Solve the resulting **n simultaneous equations** to get the mesh currents.

MESH ANALYSIS

A circuit with two meshes.



MESH ANALYSIS

- Apply KVL to each mesh. For mesh 1,

$$0V_1 + R_1i_1 + R_3(i_1 - i_2) - 6 = 0$$

$$(R_1 + R_3)i_1 - R_3i_2 = 6 - V_1$$

- For mesh 2,

$$R_2i_2 + V_2 + R_3(i_2 - i_1) - 6 = 0$$

$$-R_3i_1 + (R_2 + R_3)i_2 = 6 - V_2$$

MESH ANALYSIS

- Solve for the mesh currents.

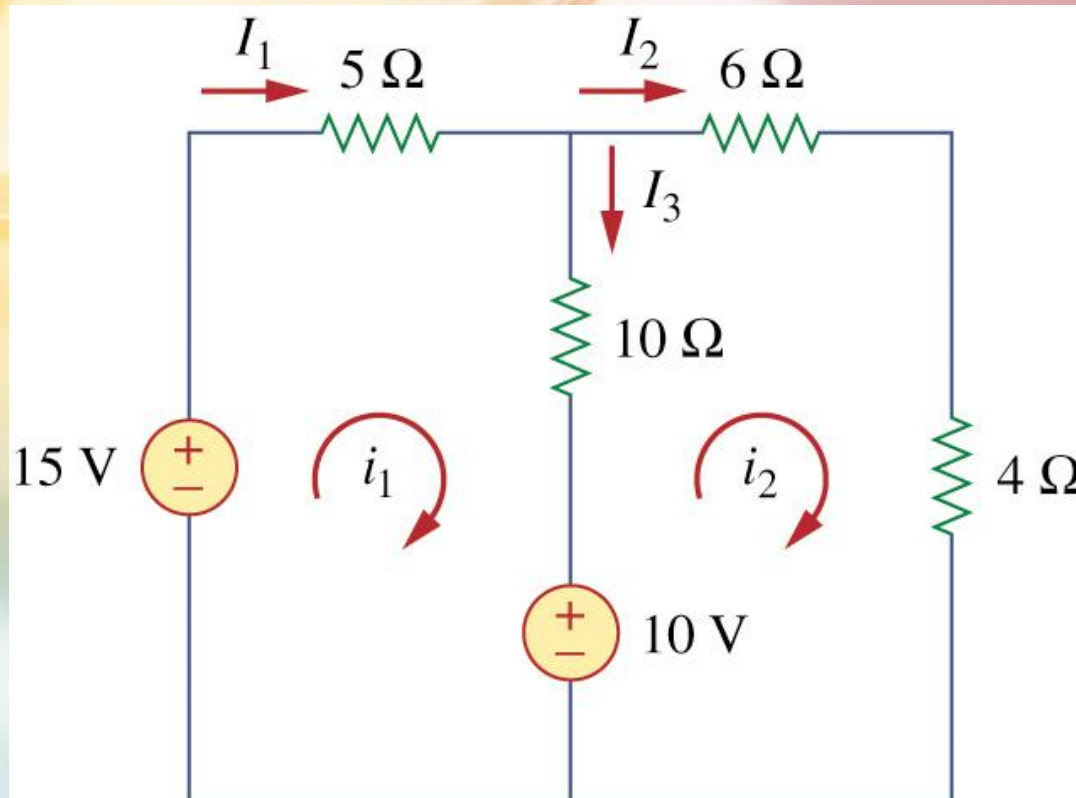
$$\begin{bmatrix} R_1 + R_3 & 0 \\ 0 & R_2 + R_3 \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} V_1 \\ 0 \end{bmatrix}$$

- Use i for a mesh current and I for a branch current. It's evident from Fig. 3.17 that

$$I_1 = i_1, \quad I_2 = i_2, \quad I_3 = i_1 - i_2$$

EXAMPLE

- Find the branch current I_1 , I_2 , and I_3 using mesh analysis.



SOLUTION

- For mesh 1,

$$015 \cdot 5i_1 \cdot 10(i_1 \ 0 \ i_2) \cdot 10 \ 6 \ 0$$

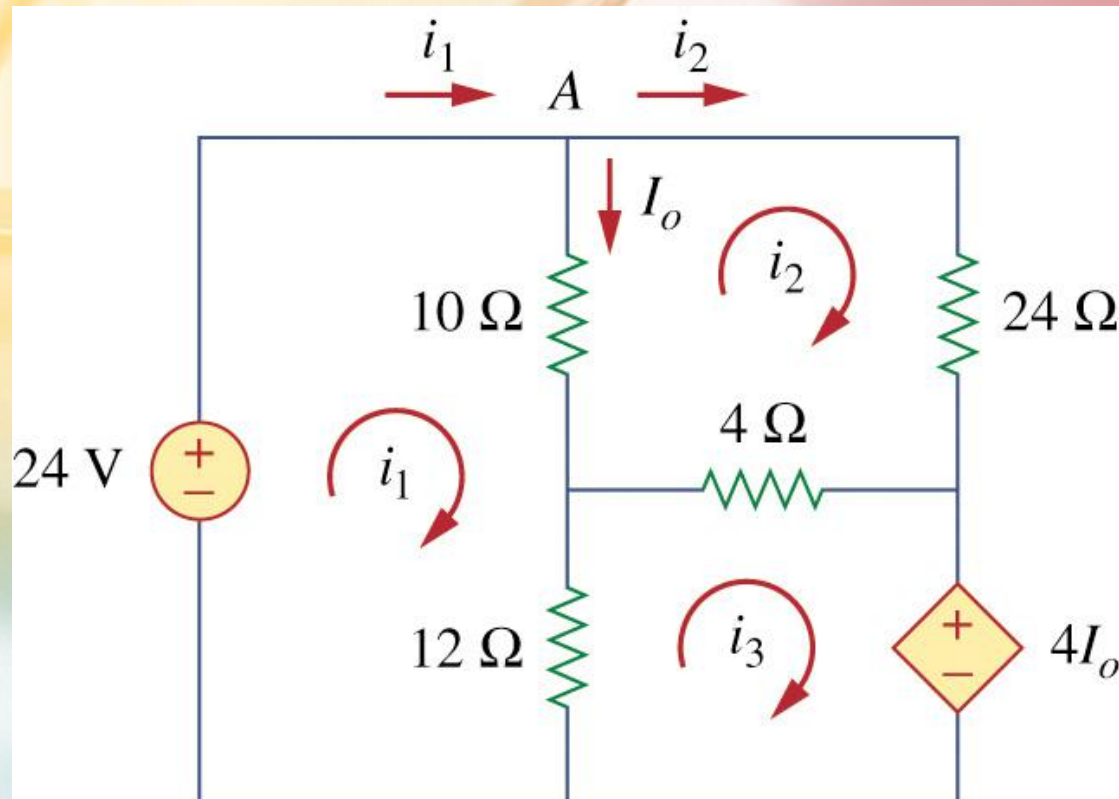
- For mesh 2 , $\boxed{3i_1 \ 0 \ 2i_2 \ 6 \ 1}$

$$6i_2 \cdot 4i_2 \cdot 10(i_2 \ 0 \ i_1) \ 0 \ 10 \ 6 \ 0$$

- We can find i_1 and i_2 by substitution method or Cramer's rule. Then, $\boxed{i_1 \ 6 \ 2i_2 \ 0 \ 1}$
 $I_1 \ 6 \ i_1, \ I_2 \ 6 \ i_2, \ I_3 \ 6 \ i_1 \ 0 \ i_2$

EXAMPLE

- Use mesh analysis to find the current I_o in the circuit.



SOLUTION

- Apply KVL to each mesh. For mesh 1,

$$0 = 24i_1 - 10(i_1 - i_2) - 12(i_1 - i_3) - 60$$

$$11i_1 - 5i_2 - 6i_3 = 60$$

- For mesh 2,

$$24i_2 - 4(i_2 - i_3) - 10(i_2 - i_1) - 60 = 0$$

$$5i_1 - 19i_2 + 2i_3 = 60$$

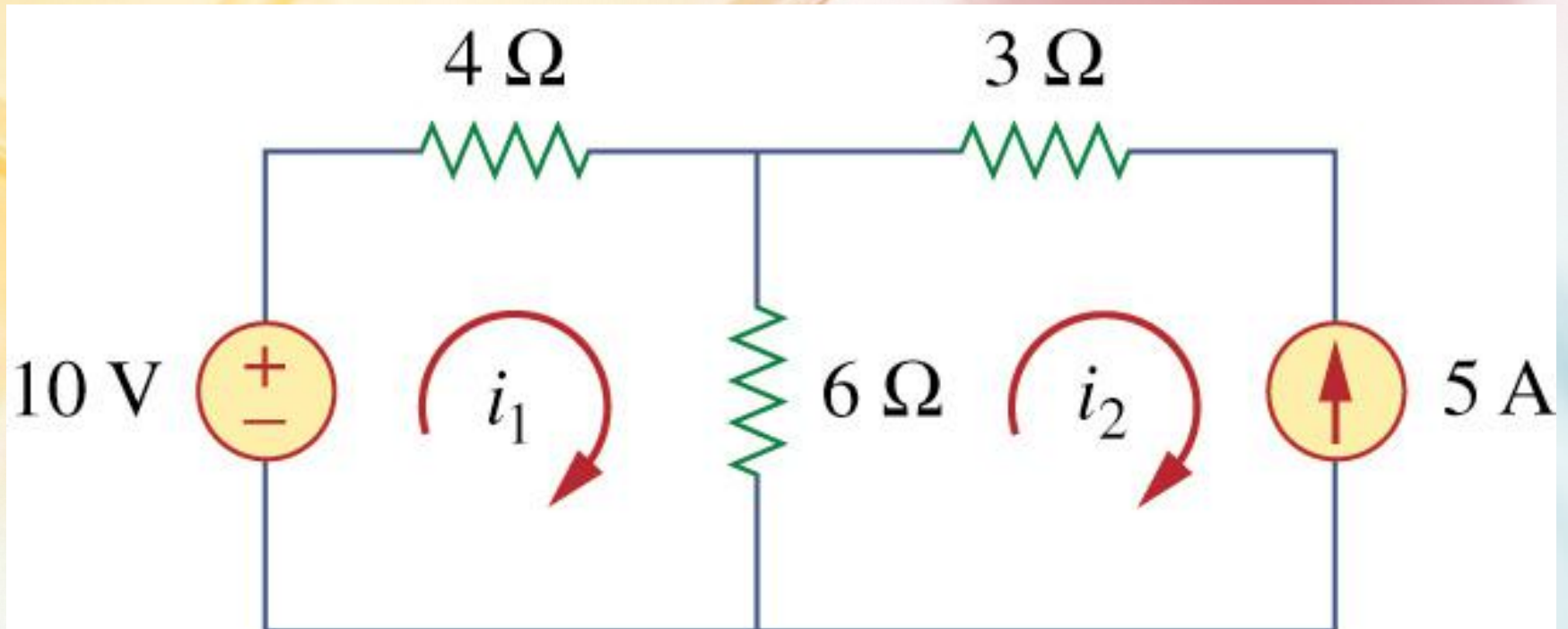
SOLUTION

- For mesh 3, $4I_0 - 12(i_3 - 0i_1) - 4(i_3 - 0i_2) = 60$
At node A, $I_0 = 6I_1 - 0i_2$,
 $4(i_1 - 0i_2) - 12(i_3 - 0i_1) - 4(i_3 - 0i_2) = 60$
- $0i_1 - 0i_2 - 2i_3 = 60$

we can calculate i_1 , i_2 and i_3 by Cramer's rule, and find I_0 .

MESH ANALYSIS WITH CURRENT SOURCES

A circuit with a current source.



MESH ANALYSIS WITH CURRENT SOURCES

- Case 1

- Current source exist only in one mesh

$$i_1 = 6.02 \text{ A}$$

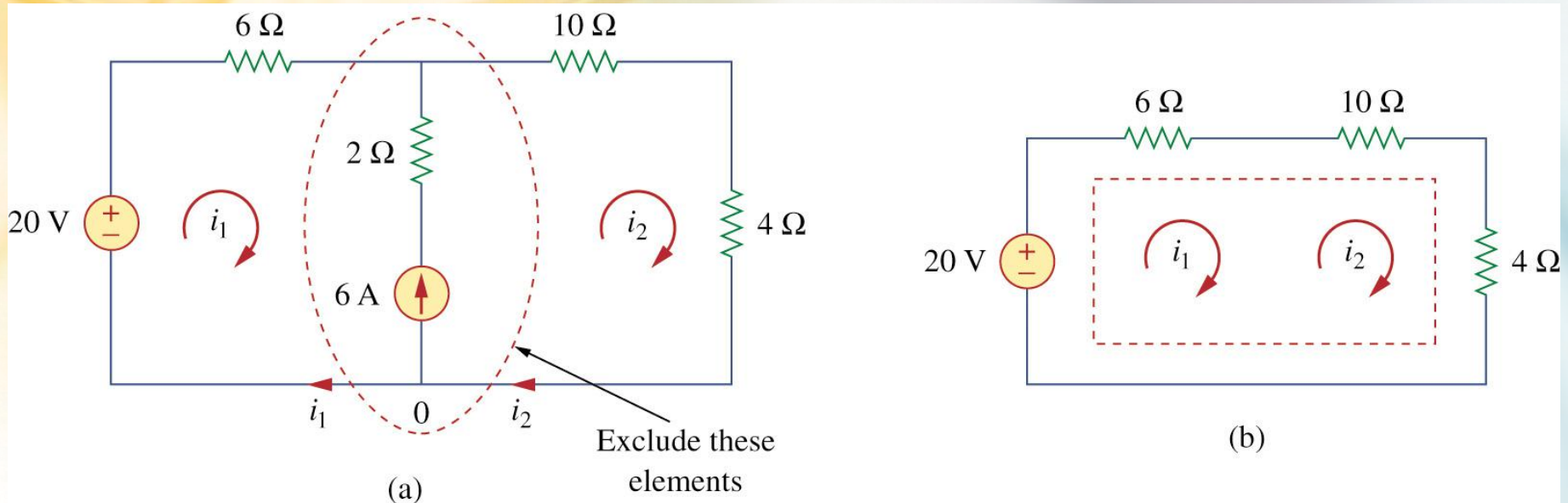
- One mesh variable is reduced

- Case 2

- Current source exists between two meshes, a **super-mesh** is obtained.

MESH ANALYSIS WITH CURRENT SOURCES

- a **supermesh** results when two meshes have a (dependent , independent) current source in common.



NODAL VERSUS MESH ANALYSIS

- Both nodal and mesh analyses provide a **systematic way** of analyzing a complex network.
- The choice of the better method dictated by two factors.
 - First factor : nature of the particular network. The key is to select the method that results in the **smaller number of equations**.
 - Second factor : **information required**.

SUMMERY

1. Nodal analysis: the application of KCL at the nonreference nodes
 - A circuit has fewer node equations
2. A supernode: two nonreference nodes
3. Mesh analysis: the application of KVL
 - A circuit has fewer mesh equations
4. A supermesh: two meshes